

The Hajvery University Lahore

Main Campus

BE Electrical - Result (Winter -2018)

8th Semester (Batch - 9, Sec-II)

Sr. #	Roll No	Name	VLSI Design (Theory+Lab)	INDUSTRIAL ELECTRONICS (Theory+Lab)	FINAL YEAR PROJECT
BE (Hons.) Electrical 8th Semester (Batch - 9, Sec-II)					
1	115-24405		C	A	A
2	115-24406		B+	C	B
3	115-24407		B+	C	A
4	115-24409		C-	F	C-
5	214-24360		B+	A-	A
6	314-24377		C-	B	B
7	314-24378		A	A	A
8	314-24381		C-	A	B+
9	314-24387		A	A	A
10	314-24388		C-	A	A
11	314-24389		B+	C	C+
12	314-24390		A	B-	C
13	314-24391		A-	B	B
14	314-24392		B	B	B
15	314-24393		B+	C	C
16	314-24395		B	C-	B
17	314-24396		C+	B	B
18	314-24398		B-	B+	A-
19	314-24399		B-	C+	B
20	314-24401		A	C+	A
21	314-24402		A-	C	B
22	315-24462		B+	B+	A-

Sana Iftikhar
(Exam Dept.)

Dr. Muhammad Khali Pervaiz
(Rector)

Usman M. Ghauri
(Registrar)